



ESSENSIUM

Your Needs Translated into Silicon

SoC Design Engineer Analog

Essensium is looking for an experienced Analog IC Design Engineer to strengthen its System On Chip (SOC) / System in package (SIP) activity focusing on devices that integrate complex analog (high and low frequency) and digital circuits.

This experienced Analog IC designer has the following responsibilities:

- Interacting with the System Architect, he proposes different alternative analog / RF architectures that meet the customer's requirements.
- Starting from the System Architect's system / block level requirements, he designs detailed block level circuits, optimizes them to the specification and provides a detailed characterization (pre and post layout) of the circuits according to the acceptance criteria.
- He is responsible for the proper layout and verification of the analog / RF circuits, taking technology requirements and parasitics into account.
- He is responsible for bench measurements, characterizing the analog / RF performance compared to the customer acceptance criteria.
- He interacts with the Technology Support Engineer to guarantee a close match between the post layout simulated performance and the measured performance and will be involved to solve analog yield discrepancies.

Expected skills that are required to handle the above mentioned responsibilities are:

- Solid understanding of Analog and RF architectures and implementations.
- Proven experience in state of the art Analog / RF circuit design using state of the art simulators (e.g. Cadence Spectre RF).
- Strong sub micron technology knowledge capable of linking technology impact on circuit performance.
- Experience in IC integration of analog / RF with large digital.
- Experience in analog / RF bench test characterization.
- Result oriented attitude.

Interested ?

Send your CV and application -> info@essensium.com or call +32-16-286500

